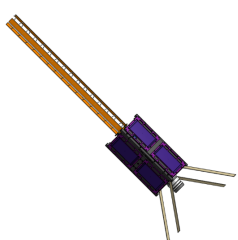


# OreSat0.5 Block Diagram

www.oresat.org  
V5.5 2023/04/25



**M0** = ST STM32F046K6 48 MHz 32 bit ARM Cortex-M0 running ChibiOS  
**G4** = ST STM32G474RBT 120 MHz 32 bit ARM Cortex-M4F  
**A8** = Octavo OSD335x-SM 1 GHz 32 bit ARM Cortex-A8 running Linux  
**OPD** = OreSat Power Domain (power bus + I2C card power control)

— = Subsystem — = PCB — = Power — = Comm. — = RF